

Form PTO-1449		ATTORNEY DOCKET NO. AUS920010716US1	SERIAL NO. <i>10/045307</i>		
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)		APPLICANT DeWitt et al.			
		FILING DATE	GROUP ART UNIT <i>214</i>		
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
<i>GAL</i>	3,707,725	12/26/1972	Dellheim	444/1	
<i>GAL</i>	4,598,364	07/01/1986	Gum et al.	364/300	
<i>GAL</i>	5,359,608	10/25/1994	Belz et al.	371/16.5	
<i>GAL</i>	5,446,876	08/29/1995	Levine et al.	395/184.01	
<i>GAL</i>	5,659,679	08/19/1997	Alpert et al.	395/183.01	
<i>GAL</i>	5,740,413	04/14/1998	Alpert et al.	395/568	
FOREIGN PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)					
<i>GAL</i>	AIX Version 4.3 Kernel and Subsystems Technical Reference, Vol. 1, "pinu Kernel Service", IBM Corp., 1997.				
<i>GAL</i>	Itanium™ Processor Microarchitecture Reference for Software Optimization, Intel Corp., 03/2000, pp. 1-28.				
<i>GAL</i>	Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 4: Itanium™ Processor Programmer's Guide, Revision 1.1, Intel Corp., 07/2000, pp. 6-1 to 6-32.				
<i>GAL</i>	Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 1: IA-64 Application Architecture, Revision 1.1, Intel Corp., 07/2000, pp. 2-1 to 2-8, 3-1 to 3-20, 4-1 to 4-35, 9-1 to 9-16, 10-1 to 10-16.				
<i>GAL</i>	Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 2: IA-64 System Architecture, Revision 1.1, Intel Corp., 07/2000, pp. 2-1 to 2-4, 3-1 to 3-22, 5-1 to 5-38, 7-1 to 7-14.				
DATE CONSIDERED <i>11/9/2004</i>	EXAMINER <i>Joshua J. Lu</i>				
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					

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		APPLICANT DeWitt et al.			
		FILING DATE	GROUP ART UNIT <i>2114</i>		
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
<i>JAC</i>	5,790,846	08/04/1998	Mealey et al.	395/591	
<i>JAC</i>	5,870,582	02/09/1999	Cheong et al.	395/394	
<i>JAC</i>	5,922,070	07/13/1999	Swoboda et al.	712/244	
<i>JAC</i>	5,938,778	08/17/1999	John, Jr. et al.	714/45	
<i>JAC</i>	5,996,092	11/30/1999	Augsburg et al.	714/38	
<i>JAC</i>	6,026,503	02/15/2000	Gutgold et al.	714/45	
<i>JAC</i>	6,055,630	04/25/2000	D'Sa et al.	712/240	
<i>JAC</i>	6,094,729	07/25/2000	Mann	714/25	
<i>JAC</i>	6,145,123	11/07/2000	Torrey et al.	717/4	
<i>JAC</i>	6,148,437	11/14/2000	Shah et al.	717/4	
FOREIGN PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
<i>JAC</i>	EP 1039386 A1	09/27/2000	EPO	_____	_____
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)					
DATE CONSIDERED <i>11/9/2004</i>	EXAMINER <i>Joshua J. Jiles</i>				
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